## The disadvantages of microstrip english language essay

Linguistics, English



The disadvantages of microstrip compared with waveguide are the generally lower power handling capacity, and higher losses. Also, unlike waveguide, microstrip is not enclosed, and is therefore susceptible to cross-talk and unintentional radiation. For lowest cost, microstrip devices may be built on an ordinary FR-4 (standard PCB) substrate. However it is often found that the dielectric losses in FR4 are too high at microwave frequencies, and that the dielectric constant is not sufficiently tightly controlled. For these reasons, an aluminasubstrate is commonly used. On a smaller scale, microstrip transmission lines are also built into monolithic microwave integrated circuits. Microstrip lines are also used in high-speed digital PCB designs, where signals need to be routed from one part of the assembly to another with minimal distortion, and avoiding high cross-talk and radiation. Microstrip is very similar to stripline and coplanar waveguide,[citation needed] and it is possible to integrate all three on the same substrate.

## Contents

[hide]1 Inhomogeneity2 Characteristic Impedance3 Bends4 See also5 References6 External links[edit]InhomogeneityThe electromagnetic wave carried by a microstrip line exists partly in the dielectric substrate, and partly in the air above it. In general, thedielectric constant of the substrate will be different (and greater) than that of the air, so that the wave is travelling in an inhomogeneous medium. In consequence, the propagation velocity is somewhere between the speed of radio waves in the substrate, and the speed of radio waves in air. This behaviour is commonly described by stating the effective dielectric constant (or effective relative permittivity) of the microstrip; this being the dielectric constant of an equivalent

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homogeneous medium (i. e., one resulting in the same propagation velocity). Further consequences of an inhomogeneous medium include: The line will not support a true TEM wave; at non-zero frequencies, both the E and H fields will have longitudinal components (a hybrid mode).[2] The longitudinal components are small however, and so the dominant mode is referred to as quasi-TEM. The line is dispersive. With increasing frequency, the effective dielectric constant gradually climbs towards that of the substrate, so that the phase velocity gradually decreases [2][3] This is true even with a nondispersive substrate material (the substrate dielectric constant will usually fall with increasing frequency). The characteristic impedance of the line changes slightly with frequency (again, even with a non-dispersive substrate material). The characteristic impedance of non-TEM modes is not uniquely defined, and depending on the precise definition used, the impedance of microstrip either rises, falls, or falls then rises with increasing frequency. [4] The low-frequency limit of the characteristic impedance is referred to as the quasi-static characteristic impedance, and is the same for all definitions of characteristic impedance. The wave impedance varies over the crosssection of the line.[edit]Characteristic ImpedanceA closed-form approximate expression for the quasi-static characteristic impedance of a microstrip line was developed by Wheeler: [5][6][7]Z extrm{microstrip} = frac{Z {0}}{2 pi sqrt{2 (1 + varepsilon {r})} mathrm{ln}left( 1 + frac{4 h}{w extrm{eff}} left( frac{14 + frac{8}{varepsilon {r}}11} frac{4 h}{w extrm{eff}} +  $sqrt{left(frac{14 + frac{8}{varepsilon {r}}}{11} frac{4 h}$ {w extrm{eff}} ight)^{2} +  $pi^{2}$  frac{1 + frac{1}{varepsilon {r}}}  $\{2\}\}$  ight) ight), where w mathrm {eff} is the effective width, which is the

Page 4

actual width of the strip, plus a correction to account for the non-zero thickness of the metallization: w extrm{eff} = w + t frac{1 + frac{1}}  $varepsilon {r}}{2 pi} mathrm{ln}left( frac{4 e}{sqrt{left( frac{t}{h})}}$  $ight)^{2} + left(frac{1}{pi} frac{1}{frac{w}{t} + frac{11}{10}}$  $ight)^{2}$  ight). Here Z0 is the impedance of free space,  $\varepsilon r$  is the relative permittivity of substrate, w is the width of the strip, h is the thickness (" height") of substrate, and t is the thickness of the strip metallization. This formula is asymptotic to an exact solution in three different casesw gg h, any varepsilon  $\{r\}$  (parallel plate transmission line), w ll h, varepsilon  $\{r\}$  = 1 (wire above a ground-plane) and wll h, varepsilon {r} gg 1. It is claimed that for most other cases, the error in impedance is less than 1%, and is always less than 2%.[7] By covering all aspect-ratios in one formula, Wheeler 1977 improves on Wheeler 1965[6] which gives one formula for w / h > 3. 3 and another for w / h le 3. 3 (thus introducing a discontinuity in the result at w / h = 3. 3). Nevertheless, the 1965 paper is perhaps the more often cited. A number of other approximate formulae for the characteristic impedance have been advanced by other authors. However, most of these are applicable to only a limited range of aspect-ratios, or else cover the entire range piecewise. Curiously, Harold Wheeler disliked both the terms 'microstrip' and 'characteristic impedance', and avoided using them in his papers.[edit]BendsIn order to build a complete circuit in microstrip, it is often necessary for the path of a strip to turn through a large angle. An abrupt 90° bend in a microstrip will cause a significant portion of the signal on the strip to be reflected back towards its source, with only part of the signal transmitted on around the bend. One means of effecting a low-reflection

bend, is to curve the path of the strip in an arc of radius at least 3 times the strip-width.[8] However, a far more common technique, and one which consumes a smaller area of substrate, is to use a mitred bend. http://upload. wikimedia. org/wikipedia/commons/thumb/1/1d/Microstrip-bend. svg/200px-Microstrip-bend. svg. pnghttp://bits. wikimedia. org/static-1.

22wmf1/skins/common/images/magnify-clip.pngMicrostrip 90° mitred bend. The percentage mitre is 100x/dTo a first approximation, an abrupt un-mitred bend behaves as a shunt capacitance placed between the ground plane and the bend in the strip. Mitring the bend reduces the area of metallization, and so removes the excess capacitance. The percentage mitre is the cut-away fraction of the diagonal between the inner and outer corners of the un-mitred bend. The optimum mitre for a wide range of microstrip geometries has been determined experimentally by Douville and James.[9] They find that a good fit for the optimum percentage mitre is given by M = 100 frac{x}{d} \\% =  $(52 + 65 e^{- \frac{27}{20} \frac{20}{nc}}) \$ the with the substrate dielectric constant varepsilon  $\{r\}$  le 25. This formula is entirely independent of varepsilon {r}. The actual range of parameters for which Douville and James present evidence is 0. 25 le w/h le 2. 75 and 2. 5 le varepsilon {r} le 25. They report aVSWR of better than 1.1 (i. e., a return better than -26 dB) for any percentage mitre within 4% (of the original d) of that given by the formula. At the minimum w/h of 0. 25, the percentage mitre is 98. 4%, so that the strip is very nearly cut through. For both the curved and mitred bends, the electrical length is somewhat shorter than the physical path-length of the strip.[edit]See also